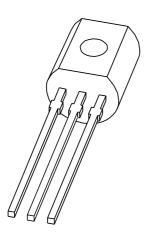
DISCRETE SEMICONDUCTORS

DATA SHEET



JC337 NPN general purpose transistor

Product specification Supersedes data of 1999 Apr 27

2004 Dec 09





NPN general purpose transistor

JC337

FEATURES

- Low current (max. 500 mA)
- Low voltage (max. 45 V).

APPLICATIONS

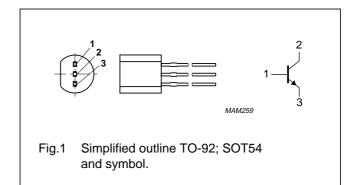
• General purpose switching and amplification, e.g. driver and output stages of audio amplifiers.

DESCRIPTION

NPN transistor in a TO-92; SOT54 plastic package. PNP complement: JC327.

PINNING

PIN	DESCRIPTION			
1	base			
2	collector			
3	emitter			



ORDERING INFORMATION

TYPE NUMBER		PACKAGE					
I TPE NOWIBER	NAME DESCRIPTION VERSION						
JC337-25	SC-43A	C-43A plastic single-ended leaded (through hole) package; 3 leads S					

NPN general purpose transistor

JC337

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS			
V _{CBO}	collector-base voltage	open emitter	_	50	V
V _{CEO}	collector-emitter voltage	open base	_	45	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	1	А
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	625	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	200	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 20 V; I _E = 0 A	_	_	100	nA
		$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	_	_	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	_	_	100	nA
h _{FE}	DC current gain	V _{CE} = 1 V;				
		I _C = 100 mA	160	_	400	
		I _C = 500 mA	40	_	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}$	_	_	700	mV
V_{BE}	base-emitter voltage	V _{CE} = 1 V; I _C = 500 mA; note 1	_	_	1.2	V
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	_	5	_	рF
f _T	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz}$	100	_	_	MHz

Note

1. V_{BE} decreases by about 2 mV/K with increasing temperature.

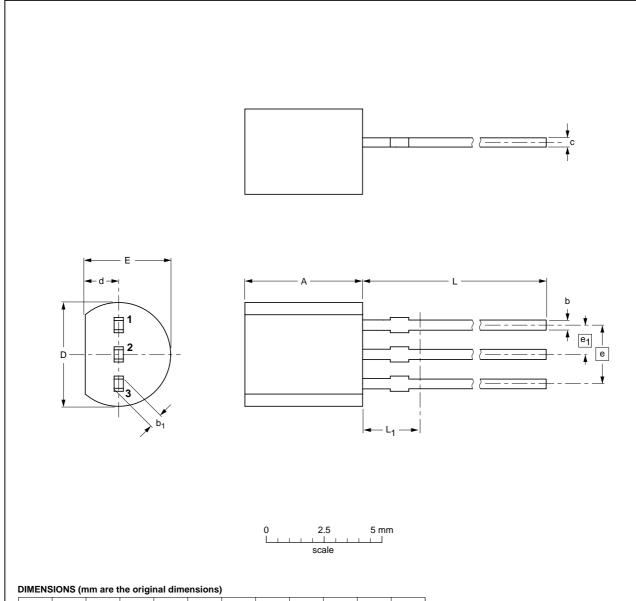
NPN general purpose transistor

JC337

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max.	
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5	

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT54		TO-92	SC-43A			04-06-28 04-11-16

NPN general purpose transistor

JC337

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Notes

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DEFINITIONS

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SCA76

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Printed in The Netherlands

R75/04/pp6

Date of release: 2004 Dec 09

Document order number: 9397 750 13604

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